IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

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:	To Be Assigned) INFORMATION
:	October 31, 2003	DISCLOSURE STATEMENT
:	SEMICONDUCTOR DEVICE)
:	OHG 138)
	•	: October 31, 2003 : SEMICONDUCTOR DEVICE

Commissioner for Patents P. O. Box 1450 Alexandria VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of one Japanese patent publication and two Japanese-language articles. Any relevance of the Japanese patent publication can be gleaned from the attached English-language Abstract. Any relevance of Japanese-language articles can be gleaned from pages 1 and 2 of the present application, where these documents are discussed. The publication and articles are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 for consideration is one U.S. patent. Any relevance of this U.S. patent is self-evident.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted documents is respectfully requested.

Respectfully submitted.

October 31, 2003

Date

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket OHG 138		Application No. To Be Assigned							
									Applicant Yoshinori SHIZUNO				
				Filing Date Group Unit									
								October 31, 2003	3	To Be Assigned			
				U.S. PATENT DOCUMENTS									
Examiner Initial		Document Number	Date	Nam	ne	Class	Sub- Class	Filing Date					
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